



#5

PATENT
P56639

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

HYUN KIM

Serial No.: 10/082,360

Examiner: *to be assigned*

Filed: 26 February 2002

Art Unit: 2622

For: METHOD AND APPARATUS FOR CORRECTING SCANNING ERROR IN
FLATBED SCANNER

INFORMATION DISCLOSURE STATEMENT

Mail Stop: Non-Fee Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED

JUL 24 2003

Technology Center 2600

Sir:

Pursuant to 37 C.F.R. §§ 1.56, and 1.97 and 1.98 applicant cites, lists, and discusses and encloses copies of the following art references cited in an *Notification of the Reasons for Objection* from the Japanese Patent Office issued on the 7th of July 2003 in corresponding Japanese patent application No. 2001-382749.

JAPANESE PATENT REFERENCES:

<u>Publication No.</u>	<u>Inventor</u>	<u>Published Date</u>
• JP 62-043965	Takeda <i>et al.</i>	25 February 1987
"Optical System Adjusting Device For Picture Reader"		
and English language Abstract for JP 62-043965.		

- JP 10-16067 Sugiura 26 November 1991
"Image Reader With Platen Criterial Mark For Determining Document Scan Start"
and English language Abstract for JP 10-16067.
- JP 10-271283 Hashimoto *et al.* 9 October 1998
"Image Reader"
and English language Abstract for JP 10-271283.
- JP 2000-115477 Takeuchi 21 April 2000
"Image Reader, Its Control Method And Storage Medium"
and English language Abstract for JP 2000-115477.
- JP 10-093787 Nishikawa 10 April 1998
"Image Reader"
and English language Abstract for JP 10-093787.

OTHER DOCUMENTS:

- Office action and *Notification of the Reasons for Objection* issued by the Japanese Patent Office on the 7th day of July 2003 in corresponding co-pending Japanese patent application assigned serial No. 2001-382749.

DISCUSSION

As explained by the Japanese patent Examiner in *the Notification of the Reasons for Objection* issued on the 7th of July 2003 in corresponding co-pending Japanese patent application No. 2001-382749, Takeuchi JP '477 "shows the flat bed scanner with the apparatus which corrects the

scanner error that has white shading with black mark (black patch), detects the edges and determines the scan start line."

The Japanese Examiner stated that Hashimoto *et al.* JP '283 "shows that it detects the edge lines on the side wall and outputs the virtual area start signal" in correspondence with applicant's Japanese claims 6, 7 and 16.

The Japanese Examiner also wrote that Sugiura JP '067 " shows that the flat bed scanner as the apparatus which detects the scanner error has the white shading with black mark (black patch) and especially the pattern (7a) for detecting scale factor" corresponding to applicant's Japanese claims 10, 11 and 18.

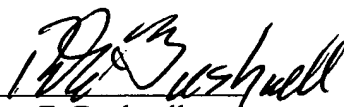
The Japanese Examiner further wrote that Takeda JP '965 and Nishikawa JP '787 "show that compare the data which is obtained by reading the pattern and the reference point, and correct them", and that Nishikawa JP '787 "shows that corrects image data with the obtained correction point" in correspondence with applicant's Japanese claim 12.

Pursuant to 37 CFR § 1.97 (e)(1), each item of information contain the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign patent application not more than three months prior to the filing of the Information Disclosure Statement.

The citation of forgoing references is not tended to constitute representation to the Examiner that a search of the prior art has been made by the Applicant. Accordingly, the U.S. Examiner is requested to make a thorough and wide-ranging search of the prior art during the examination.

No fee is incurred by filing this Information Disclosure Statement.

Respectfully submitted,


Robert E. Bushnell
Reg. No.: 27,774

1522 "K" Street, N.W., Suite 300
Washington, D.C. 20005
Area Code: 202-408-9040

Folio: P56639
Date: 22 July 2003
I.D.: REB/asc

